

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application. No:	10/086,174
Filed:	February 28, 2002
Inventor(s):	Michael G. Lavelle, Ewa Kubalska & Yan Yan Tang
Title:	Active Block Write-Back from SRAM Cache to DRAM

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|-----------------|---------------------|
| Examiner:       | Chauhan, Ulka J.    |
| Group/Art Unit: | 2671                |
| Atty. Dkt. No:  | 5181-86900<br>P6060 |

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Alexandria, VA 22313-1450 on the date indicated below.

Jeffrey C. Hood

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| <hr style="width: 20%; margin: 0 auto;"/><br>Date | <hr style="width: 20%; margin: 0 auto;"/><br>Signature |
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## **REQUEST FOR SIGNED FORM PTO 1449**

### Mail Stop Issue Fee

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant notes that the Examiner has not acknowledged receipt of all of the Information Disclosure Statements filed for the above-captioned application. Applicant has attached the unacknowledged Form PTO-1449. The form lists references A1 and A2 (sent on October 8, 2003; received by the PTO on October 10, 2003 as indicated on the enclosed copy of the return postcard).

Applicant respectfully requests a signed, initialed copy of the Form PTO-1449 previously submitted for the above-captioned application.

Respectfully submitted,

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Inventor(s): Michael G. Lavelle, Ewa Kubalska & Yan Yan Tang  
Assignee: Sun Microsystems, Inc.  
Serial No.: 10/086,174  
Filing Date: February 28, 2002  
Title: Active Block Write-Back from SRAM Cache to DRAM  
Atty. Dkt. No.: 5181-86900/P6060

The date stamp of the mail room of the U.S. Patent and Trademark Office hereon will acknowledge receipt of the attached: 1) Information Disclosure Statement; 2) PTO Form-1449 with References A1-A2; and 3) Return Postcard.

JCH/gm

Via First Class Mail

Date:

10-8-03

